WEST Refine Search Page 1 of 2

Refine Search

Search Results -

Terms	Documents	
L28 and L29	33	

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L30		<u>^</u>	Refine Search
Recall Text	Clear		Interrupt

Search History

DATE: Monday, September 25, 2006 **Purge Queries** Printable Copy Create Case

Set Name side by side	Query	<u>Hit</u> Count	<u>Set</u> <u>Name</u> result set
DB=PGPB, USPT, USOC, EPAB, JPAB, DWPI, TDBD; PLUR=YES; OP=OR			
<u>L30</u>	128 and L29	33	<u>L30</u>
<u>L29</u>	127 or 123 or 122 or 121 or 117 or 116 or 115 or 16	51	<u>L29</u>
<u>L28</u>	@ad<20040316	25507070	<u>L28</u>
<u>L27</u>	14 and L26	31	<u>L27</u>
<u>L26</u>	124 and L25	2238	<u>L26</u>
<u>L25</u>	(bus\$4 bridge\$1) same (diagnos\$5 bus\$5)	1126888	<u>L25</u>
<u>L24</u>	(diagnos\$5 bus\$4) and 17	4317	<u>L24</u>
<u>L23</u>	17 and L18	2	<u>L23</u>
<u>L22</u>	17 and L19	9	<u>L22</u>
<u>L21</u>	17 and L20	9	<u>L21</u>
<u>L20</u>	diagnos\$5 same trigger\$5 same exception\$1	69	<u>L20</u>
<u>L19</u>	captur\$5 near4 (profil\$5 near3 data)	248	<u>L19</u>
	(halt\$5 near3 process\$5) same (detect\$5 near4 (predetermin\$5		

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<u>L18</u>	condition\$1))	192	<u>L18</u>
<u>L17</u>	15 and L12	2	<u>L17</u>
<u>L16</u>	15 and L11	4	<u>L16</u>
<u>L15</u>	15 and L10	2	<u>L15</u>
<u>L14</u>	15 and L13	42	<u>L14</u>
<u>L13</u>	williams.in.	551856	<u>L13</u>
<u>L12</u>	swaine.in.	213	<u>L12</u>
<u>L11</u>	kimelman.in.	83	<u>L11</u>
<u>L10</u>	allue.in.	17	<u>L10</u>
<u>L9</u>	13 and L8	12	<u>L9</u>
<u>L8</u>	l4 and L7	45	<u>L8</u>
<u>L7</u>	11 and 12	4317	<u>L7</u>
<u>L6</u>	l4 and L5	10	<u>L6</u>
<u>L5</u>	11 same 12	712	<u>L5</u>
<u>L4</u>	(processor\$1 or microprocessor\$1 or microcomputer\$1) near3 (coupl\$5 near3 (diagnos\$5 circuit\$1))	7805	<u>L4</u>
<u>L3</u>	714/\$.ccls.	56025	<u>L3</u>
<u>L2</u>	captur\$5 near3 (diagnos\$5 data)	47136	<u>L2</u>
<u>L1</u>	process\$5 same diagnos\$5	97119	<u>L1</u>

END OF SEARCH HISTORY

Refine Search

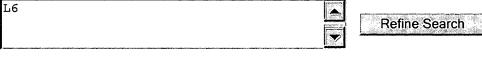
Search Results -

Terms	Documents	
L4 and L5	10	

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Set Name	Query	<u>Hit Count</u>	Set Name	
side by side			result set	
DB = PGPB, USPT, USOC, EPAB, JPAB, DWPI, TDBD; PLUR = YES; OP = OR				
<u>L6</u>	14 and L5	10	<u>L6</u>	
<u>L5</u>	@ad<20040316	25507070	<u>L5</u>	
<u>L4</u>	12 and L3	10	<u>L4</u>	
<u>L3</u>	captur\$5 near3 ((diagnos\$5 or test\$5) data)	51647	<u>L3</u>	
<u>L2</u>	(integrated circuit\$1) same L1	218	<u>L2</u>	
<u>L1</u>	multiprocess\$5 same (diagnos\$5 or test\$5)	1450	<u>L1</u>	

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L17: Entry 1 of 2 File: PGPB Sep 22, 2005

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PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20050210327 A1

TITLE: Diagnostic mechanisms within multi processing systems

PUBLICATION-DATE: September 22, 2005

INVENTOR-INFORMATION:

NAME CITY STATE COUNTRY Allue, Conrado Blasco Austin TXUS Kimelman, Paul Alamo CA US Swaine, Andrew Brookfield Cambridge GB Williams, Michael John Ely GB

ASSIGNEE-INFORMATION:

NAME CITY STATE COUNTRY TYPE CODE

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REPRESENTATIVE-FIGURES: 1

ABSTRACT:

Within a system-on-chip device 2 having multiple processing circuits 4, 6, 8, one processing circuit 4 may serve to perform diagnostic operations upon another processing circuit 8 by accessing diagnostic data relating to that other circuit. Thus, one processor may, for example, control and perform halting mode type diagnostic or code profiling upon another.

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